

HUNG-YI LEE

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EDUCATION

University of Florida

FL, U.S.

Degree: Ph.D. Aug 2017 - present
Major: Industrial and Systems Engineering
Research Project: Distributed Data-Driven Decision-Making
and High-Dimensional Learning

National Cheng Kung University

Tainan, Taiwan

Degree: Master of Science July 2011 – Aug 2013
Major: Industrial and Information Management
Overall GPA: 3.67/4.0

Academic Awards

- **Best Paper Award**, Master Thesis Competition by the OR Society of Taiwan (ORSTW), Sep, 2013
- **3rd Place Award**, Conference Paper Competition (in the Annual Meeting) by Chinese Institute of Transportation (Dec, CIT), Dec, 2012
- **3rd Place Award**, INFORMS Railway Applications Section Problem Solving Competition, Nov, 2011

WORK EXPERIENCE

Scheduling System Engineer

Hsinchu, Taiwan

**Department of Computer Integration Manufacturing (CIM),
Taiwan Semiconductor Manufacturing Co. Ltd. (TSMC)** Sep 2014 – May 2016

- Develop scheduling sub-system for the production control from FEOL (Front End of Line) to BEOL (Back End of Line) of the entire wafer fabrication process
- Build IP model combining commercial solver, CPLEX, to improve the efficiency of production in the Photo section of the wafer production line
- Implement heuristic approach to solve the scheduling problems in SEZ (Semiconductor Equipment Zubehör) tool groups

PUBLICATIONS

1. High-Dimensional Learning with Linear Constraints (under review)

PRESENTATIONS

1. High-Dimensional Learning with Linear Constraints; INFORMS Annual Meeting 2018

ADDITIONAL SKILLS

Computer Language	C/C++, C#, Java, Python
Commercial Optimizer	CPLEX, GUROBI
Language	Mandarin Chinese (Native), English